



Eastern Mediterranean University

"Virtue, Knowledge, Advancement"

Department of Information Technology

ITEC259 Digital Logic Design

List of Experiments

EXP.	Experiment Title	Date
0	Demonstration of the Program	19 October 2020
1	Introduction to Logic Gates	26 October 2020
2	Simplification of Boolean Functions	2 November 2020
3	Implementation of Boolean Functions	9 November 2020
4	Applying NAND and NOR gates	16 November 2020
5	Logic Simplification with Karnaugh Maps	23 November 2020
6	Expressing Boolean Functions as Sum of Minterms	14 December 2020
7	Exclusive-OR	21 December 2020
8	Decoders	28 December 2020
9	Multiplexers	4 January 2020
10	D-type, JK-type & T-type Flip-Flops	11 January 2020
-	Lab Make-ups	18 January 2020

Notes:

1. Only one make-up is allowed for the missed labs.
2. You are expected to work alone
3. You should fill the provided lab reports for the experiments and submit them as a single file electronically in PDF at the end of each experiment.
4. For any announcement check out <https://lms.emu.edu.tr> and <http://staff.emu.edu.tr/ahmetrizaner/en/Pages/ITEC259.aspx>.
5. LogicWorks (An interactive circuit design tool, <https://designworkssolutions.com>) will be used for the lab experiment, however you may also use "Logic Circuits (educational software for designing and simulating digital logic circuits, <http://www.logiccircuit.org>), or CircuitVerse (Free online logic simulator, <https://circuitverse.org>)