**CMPE224 DIGITAL LOGIC SYSTEMS**

1. **Introduction to Quartus Program**

Quartus 13.0sp1 web edition

<https://www.intel.com/content/www/us/en/collections/products/fpga/software/downloads.html?edition=web&platform=windows&download_manager=direct&s=Newest&p=7>

1. **Create a Project**
2. **How to Create a Schematic / Block Diagram**
3. **How to Create a VERILOG HDL (lab 4)**
4. **Design Block Diagram/VERILOG HDL**
5. **Compiling the Project**
6. **Simulating the Project**